side by side result set

DB=USPT; PLUR=YES; OP=ADJ

L76	pipeline adj stages and comparison adj logic and interface and memory	77	L76
L75	L71 and tag	3	L75
L74	L72 and tag	4	L74
L73	L72 and tagging	0	L73
L72	L68 and rate	5	L72
L71	L70 and rate	3	L71
L70	L67 and comparison adj logic	3	L70
L69	L68 and comparison adj logic	0	L69
L68	L67 and header	8	L68
L67	L66 and memory	38	L67
L66	pipeline adj interface and stages	44	L66
L65	pipeline adj stage adj interface	0	L65
L64	L63 and tagging	1	L64
L63	L61 and rate	10	L63
L62	L61 and tagging	1	L62
L61	L60 and flow	10	L61
L60	L59 and memory	10	L60
L59	L58 and header	10	L59
L58	pipeline adj stages and comparison adj logic and interfaces	77	L58
L57	L54 and input adj capacity	0	L57
L56	L54 and input adj rate	0	L56
L55	L54 and rate	1	L55
L54	pipeline adj stages and comparison adj logic and first adj interface and second adj interface	1	L54
L53	L52 and pipeline	0	L53
L52	comparison adj logic and interfaces and input adj capacity and size	1	L52
L51	L50 and tag and logic	58	L51
L50	L48 and flow	70	L50
L49	L48 and input adj flow	0	L49
L48	L44 and memory	72	L48
L47	L44 and comparison adj logic	0	L47
L46	L43 and size and input adj rate	0	L46
L45	L43 and size and input adj capacity	0	L45
L44	L43 and size and capacity	73	L44
L43	L42 and interface	216	L43
L42	pipeline adj stages and header	232	L42

L41	L40 and pipeline	0	L41
L40	input adj capacity and header and packet adj size	6	L40
L39	L37 and header	0	L39
L38	L37 and pipeline	0	L38
L37	input adj flow and memory and input adj capacity	4	L37
L36	L34 and capacity and size	22	L36
L35	L34 and input adj capacity	0	L35
L34	L30 and header	39	L34
L33	L32 and header	0	L33
L32	L30 and input adj rate	4	L32
L31	L30 and input adj capacity	0	L31
L30	L29 and interface	149	L30
L29	comparison adj logic and pipeline and stages	175	L29
L28	L26 and comparison adj logic	0	L28
L27	L26 and header	3	L27
L26	L25 and interface and packet adj size	4	L26
L25	L24 and stages	214	L25
L24	input adj rate and pipeline	287	L24
L23	input adj rate adj regulation and pipeline	0	L23
L22	L21 and header and interface	1	L22
L21	pipeline and stages and input adj capacity	16	L21
L20	L19 and header	0	L20
L19	L18 and input	14	L19
L18	L17 and comparison adj logic	14	L18
L17	L16 and capacity and size	561	L17
L16	pipeline adj stages and interface	1758	L16
L15	L14 and comparison adj logic	0	L15
L14	L13 and interface	8	L14
L13	input adj capacity and packet adj size	9	L13
L12	L11 and input adj capacity	0	L12
L11	pipeline adj stages and interface	1758	L11
L10	input adj capacity and interface and memory and header	13	L10
L9	input adj capacity and pipeline adj stages	0	L9
L8	L7 and capacity	5	L8
L7	L5 and packet adj size	33	L7
L6	L5 and input adj capacity	0	L6
L5	L4 and interface	1758	L5
L4	pipeline adj stages	2596	L4

L3	pipeline adj stages and interface and input adj capacity and packet adj size	0	L3
L2	pipeline and stages and interface	6036	L2
L1	compare and input adj capacity and packet adj size	2	L1

END OF SEARCH HISTORY